



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hideto Ohnuma et al.

Serial No. : 09/050,182

Filed : March 26, 1998

Art Unit : 1765

Examiner : Robert Kunemund

Confirmation No.: 6531

Notice of Allowance Date: October 19, 2004

Title : METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE

MAIL STOP ISSUE FEE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

TRANSMITTAL OF FORMAL DRAWINGS

In response to the Notice of Allowability mailed October 19, 2004, please substitute the enclosed eighteen (18) sheets of formal drawings, including Figs. 1-18F, for the corresponding drawings presently in the application.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: January 19, 2005



John F. Hayden
Reg. No. 37,640

Customer No. 26171

Fish & Richardson P.C.

1425 K Street, N.W., 11th Floor

Washington, DC 20005-3500

Telephone: (202) 783-5070

Facsimile: (202) 783-2331